

Date: January 30, 2014
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Test Overview

This report is a summary of all testing activities and makes use of several other documents which will be included as part of the Final Report package.

2.0b Profile:

Test 1: 2.0B VEN – Pull - Universal Devices, Inc. – ISY vOADR2B_21 – PASSED

Test 2: 2.0B VEN – Push - Universal Devices, Inc. – ISY vOADR2B_21 – PASSED

Test Method: VEN

Product Name: ISY

Product Model Number: 994 Z Series

Firmware Version: OADR2B_21

Profile: 2.0b

Test Harness: 1.0.5

Test Environment: ISY994Z Dashboard

Test Results

2.0b VEN Pull Results:

Test Case Name	Result
N1_0010_TH_VTN_1	PASS
N1_0020_TH_VTN_1	PASS
N1_0050_TH_VTN_1	PASS
N1_0060_TH_VTN_1	PASS
N1_0065_TH_VTN_1	PASS
N1_0070_TH_VTN_1	PASS
N1_0080_TH_VTN_1	NA
N1_0025_TH_VTN_1	PASS
N1_0015_TH_VTN_1	PASS
N1_0030_TH_VTN_1	PASS
N1_0020_TH_VTN_1	PASS
N1_0040_TH_VTN_1	PASS
N1_0020_TH_VTN_1	PASS
P1_2010_TH_VTN_1	PASS
P1_2015_TH_VTN_1	PASS
P1_2020_TH_VTN_1	PASS
P1_2030_TH_VTN_1	PASS
P1_2040_TH_VTN_1	PASS
P1_2050_TH_VTN_1	NA
R1_3010_TH_VTN_1	PASS
R1_3020_TH_VTN_1	PASS
R1_3025_TH_VTN_1	PASS
R1_3027_TH_VTN_1	PASS

R1_3030_TH_VTN_1	PASS
R1_3040_TH_VTN_1	PASS
R1_3045_TH_VTN_1	PASS
R1_3050_TH_VTN_1	PASS
R1_3060_TH_VTN_1	PASS
R1_3150_TH_VTN_1	PASS
R1_3160_TH_VTN_1	PASS
R1_3170_TH_VTN_1	PASS
R1_3180_TH_VTN_1	PASS
R1_3190_TH_VTN_1	PASS
A_E1_0020_TH_VTN_1	PASS
A_E1_0040_TH_VTN_1	PASS
A_E1_0060_TH_VTN_1	PASS
A_E1_0070_TH_VTN_1	PASS
A_E1_0082_TH_VTN_1	PASS
A_E1_0086_TH_VTN_1	PASS
A_E1_0090_TH_VTN_1	PASS
A_E1_0092_TH_VTN_1	PASS
A_E1_0094_TH_VTN_1	PASS
A_E1_0096_TH_VTN_1	PASS
A_E1_0098_TH_VTN_1	PASS
A_E1_0100_TH_VTN_1	PASS
A_E1_0102_TH_VTN_1	PASS
A_E1_0104_TH_VTN_1	PASS
A_E1_0110_TH_VTN_1	PASS
A_E1_0130_TH_VTN_1	PASS
A_E1_0180_TH_VTN_1	PASS
A_E1_0190_TH_VTN_1	PASS

A_E1_0200_TH_VTN_1	PASS
A_E1_0210_TH_VTN_1	PASS
A_E1_0220_TH_VTN_1	PASS
A_E1_0230_TH_VTN_1	PASS
A_E1_0240_TH_VTN_1	PASS
A_E1_0250_TH_VTN_1	PASS
A_E1_0260_TH_VTN_1	PASS
A_E1_0260_TH_VTN_1	PASS
A_E1_0260_TH_VTN_1	PASS
A_E1_0262_TH_VTN_1	PASS
A_E1_0262_TH_VTN_1	PASS
A_E1_0262_TH_VTN_1	PASS
A_E1_0267_TH_VTN_1	PASS
A_E1_0267_TH_VTN_1	PASS
A_E1_0267_TH_VTN_1	PASS
A_E1_0268_TH_VTN_1	PASS
A_E1_0268_TH_VTN_1	PASS
A_E1_0268_TH_VTN_1	PASS
A_E1_0270_TH_VTN_1	PASS
A_E1_0285_TH_VTN_1	PASS
A_E1_0300_TH_VTN_1	PASS
A_E1_0310_TH_VTN_1	PASS
A_E1_0280_TH_VTN_1	PASS
A_E1_0325_TH_VTN_1	PASS
A_E1_0340_TH_VTN_1	PASS
A_E1_0345_TH_VTN_1	PASS
A_E1_0360_TH_VTN_1	PASS
A_E1_0370_TH_VTN_1	PASS

A_E1_0390_TH_VTN_1	PASS
A_E1_0392_TH_VTN_1	PASS
E1_1010_TH_VTN_1	PASS
E1_1020_TH_VTN_1	PASS
E1_1027_TH_VTN_1	PASS
E1_1030_TH_VTN_1	PASS
E1_1040_TH_VTN_1	PASS
E1_1050_TH_VTN_1	PASS
E1_1060_TH_VTN_1	PASS
E1_1065_TH_VTN_1	NA
E1_1070_TH_VTN_1	PASS
E1_1080_TH_VTN_1	PASS
G1_4010_TH_VTN_1	PASS
G1_4015_TH_VTN_1	PASS

2.0b VEN Push Results:

Test Case Name	Result
N0_5015_TH_VTN_1	PASS
N0_5050_TH_VTN	PASS
N0_5060_TH_VTN_1	PASS
N0_5065_TH_VTN_1	PASS
N0_5070_TH_VTN_1	PASS
N0_5080_TH_VTN_1	PASS
N0_5010_TH_VTN_1	PASS
N0_5020_TH_VTN_1	PASS
N0_5030_TH_VTN_1	PASS
N0_5025_TH_VTN_1	PASS
N0_5040_TH_VTN	PASS

N0_5020_TH_VTN_1	PASS
P0_7010_TH_VTN_1	PASS
P0_7015_TH_VTN_1	PASS
P0_7020_TH_VTN_1	PASS
P0_7050_TH_VTN_1	NA
R0_8010_TH_VTN	PASS
R0_8020_TH_VTN	PASS
R0_8025_TH_VTN	PASS
R0_8027_TH_VTN	PASS
R0_8030_TH_VTN	PASS
R0_8040_TH_VTN	PASS
R0_8045_TH_VTN	PASS
R0_8050_TH_VTN	PASS
R0_8060_TH_VTN_1	PASS
P0_7030_TH_VTN_1	PASS
P0_7040_TH_VTN_1	PASS
R0_8150_TH_VTN	PASS
R0_8160_TH_VTN	PASS
R0_8170_TH_VTN	PASS
R0_8180_TH_VTN	PASS
R0_8190_TH_VTN	PASS
A_E0_0020_TH_VTN_1	PASS
A_E0_0040_TH_VTN	PASS
A_E0_0060_TH_VTN_1	PASS
A_E0_0070_TH_VTN_1	PASS
A_E0_0082_TH_VTN_1	PASS
A_E0_0086_TH_VTN_1	PASS
A_E0_0090_TH_VTN_1	PASS

A_E0_0092_TH_VTN_1	PASS
A_E0_0094_TH_VTN_1	PASS
A_E0_0096_TH_VTN	PASS
A_E0_0098_TH_VTN_1	PASS
A_E0_0100_TH_VTN_1	PASS
A_E0_0102_TH_VTN_1	PASS
A_E0_0104_TH_VTN_1	PASS
A_E0_0110_TH_VTN_1	PASS
A_E0_0130_TH_VTN_1	PASS
A_E0_0180_TH_VTN_1	PASS
A_E0_0185_TH_VTN_1	PASS
A_E0_0190_TH_VTN_1	PASS
A_E0_0200_TH_VTN_1	PASS
A_E0_0210_TH_VTN_1	PASS
A_E0_0220_TH_VTN_1	PASS
A_E0_0230_TH_VTN	PASS
A_E0_0240_TH_VTN	PASS
A_E0_0250_TH_VTN_1	PASS
A_E0_0260_TH_VTN_1	PASS
A_E0_0260_TH_VTN_1	PASS
A_E0_0260_TH_VTN_1	PASS
A_E0_0262_TH_VTN_1	PASS
A_E0_0262_TH_VTN_1	PASS
A_E0_0262_TH_VTN_1	PASS
A_E0_0267_TH_VTN_1	PASS
G1_4010_TH_VTN_1	PASS
G0_9010_TH_VTN	PASS
A_E0_0267_TH_VTN_1	PASS

A_E0_0267_TH_VTN_1	PASS
A_E0_0268_TH_VTN_1	PASS
A_E0_0268_TH_VTN_1	PASS
A_E0_0268_TH_VTN_1	PASS
A_E0_0270_TH_VTN_1	PASS
A_E0_0280_TH_VTN_1	PASS
A_E0_0285_TH_VTN_1	PASS
A_E0_0300_TH_VTN_1	PASS
A_E0_0310_TH_VTN_1	PASS
A_E0_0325_TH_VTN_1	PASS
A_E0_0340_TH_VTN_1	PASS
A_E0_0345_TH_VTN_1	PASS
A_E0_0360_TH_VTN_1	PASS
A_E0_0370_TH_VTN_1	PASS
A_E0_0390_TH_VTN_1	PASS
A_E0_0392_TH_VTN_1	PASS
E0_6010_TH_VTN	PASS
E0_6020_TH_VTN	PASS
E0_6025_TH_VTN	PASS
E0_6027_TH_VTN	PASS
E0_6040_TH_VTN	PASS
E0_6060_TH_VTN	PASS
E0_6065_TH_VTN	NA
E0_6070_TH_VTN	PASS
E0_6080_TH_VTN	PASS
E0_6080_TH_VTN	PASS
E0_6050_TH_VTN_1	PASS
E0_6030_TH_VTN VTN	PASS

